

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Randall L. Findley, *et al.*

Serial No.: 09/977,045

Filed: October 12, 2001

Title: PROGRAMMABLE FEEDBACK DELAY PHASE-LOCKED
LOOP FOR HIGH-SPEED INPUT/OUTPUT TIMING BUDGET
MANAGEMENT AND METHOD OF OPERATION THEREOF

Grp./A.U.: 2611

Examiner: Kevin Kim

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed with United States Patent and trademark Office on:
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Sir:

AMENDMENT UNDER 37 C.F.R. § 1.116

The Applicants have carefully considered this application in connection with the Examiner's Final Rejection mailed July 11, 2006, and respectfully request reconsideration of this application in view of the following remarks.